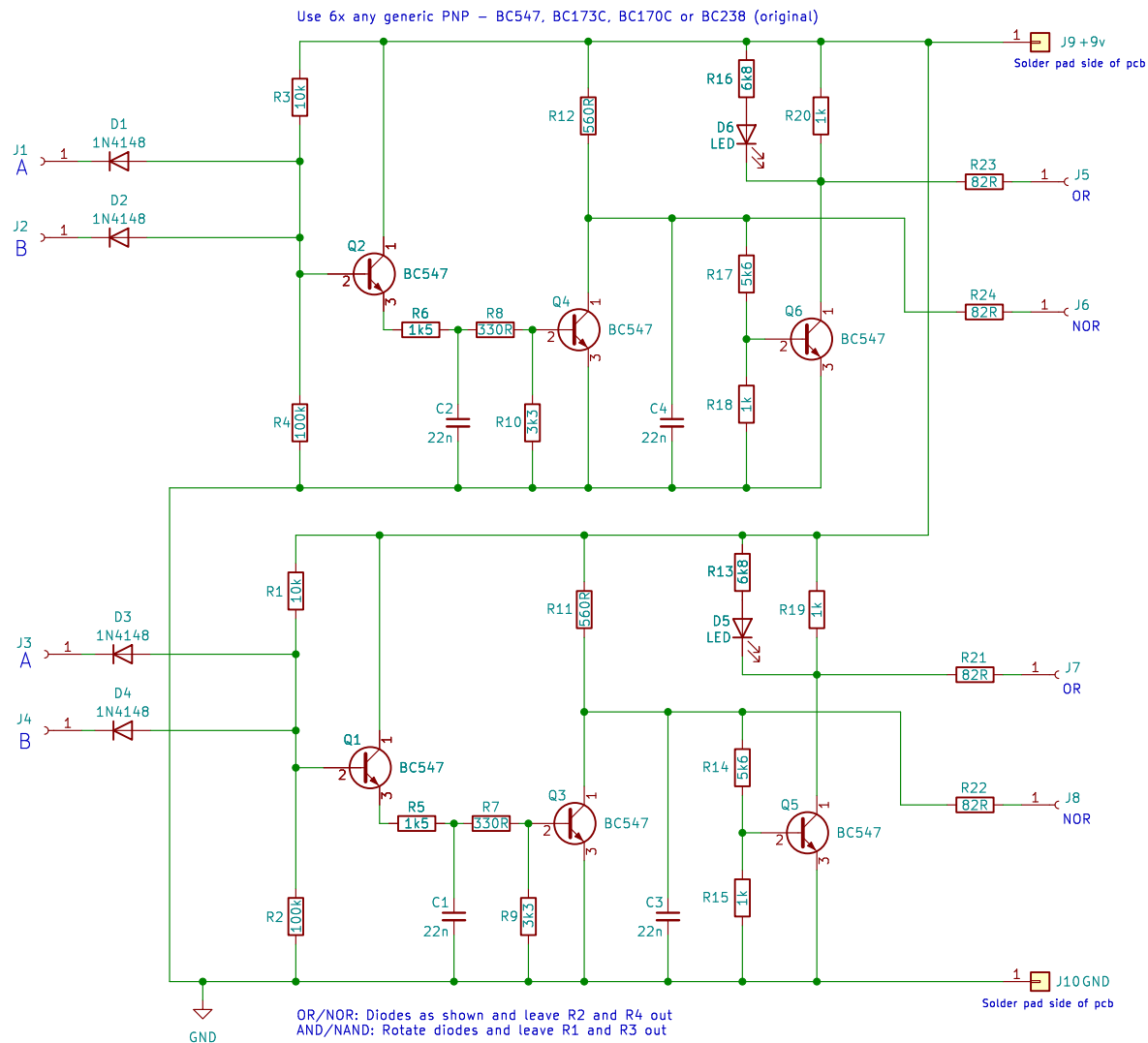




Classic Dual FT 2-input gate

NEGATIVE LOGIC

Arnoud van Delden

Whizzbizz

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File: Classic Dual FT 2-input Gate.kicad_sch

Title: Classic Dual FT 2-input gate

Size: A4

Date: 2025-08-17

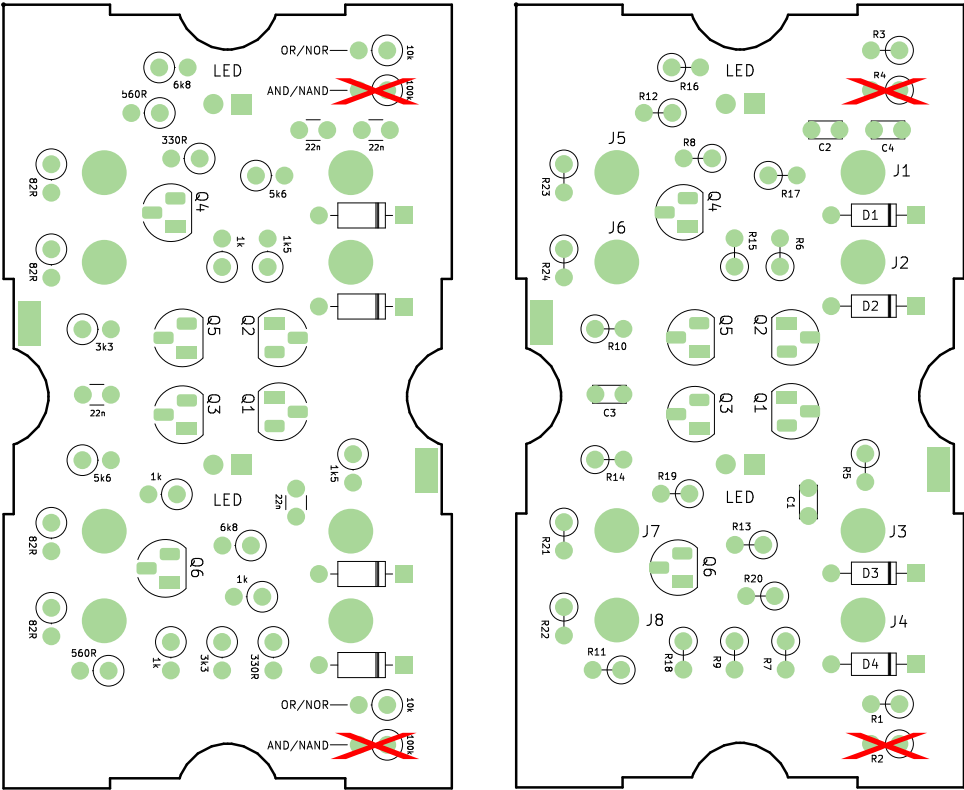
Rev:

KiCad E.D.A. kicad 7.0.10

Id: 1/1

Classic fischertechnik Dual OR/NOR "Silberling" - onderdelenlijst en componentenopstelling
Classic fischertechnik Dual OR/NOR "Silberling" - parts list and component layout
Classic fischertechnik Dual OR/NOR "Silberling" - Stückliste und Bauteilanordnung

Dual 2-input OR/NOR Gate



OR/NOR: De positie van R2 en R4 leeglaten op de print

OR/NOR: Leave the position of R2 and R4 blank on the pcb

OR/NOR: Lassen Sie die Position von R2 und R4 frei

Reference	Footprint	Quantity	Value
Silberling Case	3D Printed	1	
Module lid with front plate	3D Printed + Laminated Front	1	
Screw 2x6 for lid		4	
3mm Metal Strip Long	3mm Metal Strip Long	2	
3mm Metal Strip Short	3mm Metal Strip Short	2	
J1, J2, J3, J4, J5, J6, J7, J8	PTN2-10 pin for FT plug	8	
C1, C2, C3, C4	Small Ceramic Capacitor	4	22n
D1, D2, D3, D4	Diode	4	1N4148
D5, D6	LED 3mm	2	LED
Q1, Q2, Q3, Q4, Q5, Q6	Generic NPN Transistor TO-92	6	BC547
R1, R3	1/4W Resistor (vertically mounted)	2	10K Ω
R2, R4	Not Used	0	
R5, R6	1/4W Resistor (vertically mounted)	2	1K5 Ω
R7, R8	1/4W Resistor (vertically mounted)	2	330 Ω
R9, R10	1/4W Resistor (vertically mounted)	2	3K3 Ω
R11, R12	1/4W Resistor (vertically mounted)	2	560 Ω
R13, R16	1/4W Resistor (vertically mounted)	2	6K8 Ω
R14, R17	1/4W Resistor (vertically mounted)	2	5K6 Ω
R15, R18, R19, R20	1/4W Resistor (vertically mounted)	4	1K Ω
R21, R22, R23, R24	1/4W Resistor (vertically mounted)	4	82 Ω